

Notice of References Cited	Application/Control No. 10/786,543		Applicant(s)/Patent Under Reexamination OTANI, HIROSHI	
	Examiner Kevin Quinto		Art Unit 2826	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
X	A	US-4,391,032	07-1983	Schulte, Heinz	-
X	B	US-5,349,858	09-1994	Yagi et al.	-
X	C	US-5,545,912	08-1996	Ristic et al.	-
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	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf and Tauber, "Silicon Processing for the VLSI Era, Volume 2 - Process Integration," Lattice Press, 2000, p. 851-852
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.